



Higher
Coursework
Assessment Task



Higher Engineering Science Assignment Marking Instructions

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These marking instructions are prepared by examination teams for use by SQA appointed markers when marking external course assessments.

Please note, as we were not able to carry out live marking in 2020, these marking instructions are not presented in a final state and have not been referenced against candidate responses.

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Marking instructions

Marking instructions are provided for this specimen assessment task. In line with SQA's normal practice, they are addressed to the marker. They will also be helpful for those preparing candidates for course assessment.

Marking instructions **will not** be provided with annual assessment tasks, as candidate evidence will be submitted to SQA for external marking. They will be provided to markers and then published on the SQA website after marking is complete.

General marking principles

This information is provided to help you understand the general principles that must be applied when marking candidate responses in this assignment. These principles must be read in conjunction with the detailed/specific marking instructions, which identify the key features required in candidate responses.

- a Marks for each candidate response must **always** be assigned in line with these general marking principles and the specific marking instructions for this assessment.
- b Marking should always be positive. This means that, for each candidate response, marks are accumulated for the demonstration of relevant skills, knowledge and understanding: they are not deducted from a maximum on the basis of errors or omissions.
- c If a specific candidate response is not covered by either the general marking principles or detailed marking instructions, you must seek guidance from your team leader.

Task 1 – Camera tracking system

Task		Expected response	Max mark	Additional guidance
1	a		6	<p>Control diagram:</p> <ul style="list-style-type: none"> ◆ animal sensor (1 mark) ◆ error detector (1 mark) ◆ control box, correct order (1 mark) ◆ driver box, correct order (1 mark) ◆ motor box, correct order (1 mark) ◆ camera position sensor, feedback loop, from correct place and direction (1 mark)

Task		Expected response	Max mark	Additional guidance
1	b		2	<p>For simulated circuits;</p> <p>Input – potentiometers and voltmeter. (1 mark)</p> <p>Process – op-amp. (1 mark)</p> <p>For constructed circuits;</p> <p>Correct components included. (1 mark)</p> <p>Correct connections made. (1 mark)</p>

Task		Expected response				Max mark	Additional guidance													
1	c	<table border="1"> <thead> <tr> <th>Planned test</th> <th>Expected result</th> <th>Actual result</th> <th>Amendment made</th> </tr> </thead> <tbody> <tr> <td>Set both inputs to their lowest setting (0 V).</td> <td>Output on voltmeter should be 0 V.</td> <td>Output is 0 V.</td> <td>None required.</td> </tr> <tr> <td>Set the animal sensor input to its minimum (0 V) and the camera position sensor to its maximum (5 V).</td> <td>Output on voltmeter should be +2.5 V.</td> <td>Voltage is -10 V.</td> <td>Resistances changed to provide correct gain. Inputs swapped (or additional inverting amp added) to create positive voltage.</td> </tr> <tr> <td>Leave the animal sensor at its minimum (0 V) then reduce the camera position sensor gradually from 5 V to 0 V.</td> <td>Output on voltmeter should reduce from +2.5 V to 0 V.</td> <td>Output reduces as from 2.5 V to 0 V.</td> <td>None required.</td> </tr> </tbody> </table>	Planned test	Expected result	Actual result	Amendment made	Set both inputs to their lowest setting (0 V).	Output on voltmeter should be 0 V.	Output is 0 V.	None required.	Set the animal sensor input to its minimum (0 V) and the camera position sensor to its maximum (5 V).	Output on voltmeter should be +2.5 V.	Voltage is -10 V.	Resistances changed to provide correct gain. Inputs swapped (or additional inverting amp added) to create positive voltage.	Leave the animal sensor at its minimum (0 V) then reduce the camera position sensor gradually from 5 V to 0 V.	Output on voltmeter should reduce from +2.5 V to 0 V.	Output reduces as from 2.5 V to 0 V.	None required.	5	<p>Actual result 1: 0 V output. (1 mark)</p> <p>Actual result 2: -10 V. (1 mark)</p> <p>Amendment made 2: rewire inputs to op-amp (or extra inv). (1 mark)</p> <p>Change resistors appropriately. (1 mark)</p> <p>Actual result 3: voltage steadily decreasing. (1 mark)</p> <p>FTE - If the candidate has the voltmeter incorrectly orientated then 1 mark can be awarded for the two 'non-required' responses.</p>
		Planned test	Expected result	Actual result	Amendment made															
		Set both inputs to their lowest setting (0 V).	Output on voltmeter should be 0 V.	Output is 0 V.	None required.															
		Set the animal sensor input to its minimum (0 V) and the camera position sensor to its maximum (5 V).	Output on voltmeter should be +2.5 V.	Voltage is -10 V.	Resistances changed to provide correct gain. Inputs swapped (or additional inverting amp added) to create positive voltage.															
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Task		Expected response	Max mark	Additional guidance
1	d		2	<p>1 mark for each correctly implemented correction. (2 marks)</p> <p>Allow follow-through error based on candidate's response in test table 1c.</p>
1	e		2	<p>Correct choice of components (motor and mosfet). (1 mark)</p> <p>Correct connections to allow operation. (1 mark)</p>

Task 2 – Sensing Control System

Task		Expected response	Max mark	Additional guidance
2	a	<p>The diagram shows a logic circuit with four sensors labeled A, B, C, and D. Sensors A and B are connected to the inputs of an AND gate labeled IC2a (74HC08). Sensors C and D are connected to the inputs of a NAND gate labeled IC4a (74HC02). The output of IC2a and the output of IC4a are connected to the inputs of an OR gate labeled IC3a (74HC32). The output of IC3a is connected to a red LED indicator.</p>	1	Gates as per design and labels shown. (1 mark)

Task		Expected response						Max mark	Additional guidance
2	b							1	<p>Actual results in truth table. (1 mark)</p> <p>Marks cannot be awarded if no evidence of simulation in 2a.</p> <p>Allow follow on error based on final circuit.</p>
		A	B	C	D	Z	NAND equivalent		
		0	0	0	0	1			
		0	0	0	1	0			
		0	0	1	0	0			
		0	0	1	1	0			
		0	1	0	0	1			
		0	1	0	1	0			
		0	1	1	0	0			
		0	1	1	1	0			
		1	0	0	0	1			
		1	0	0	1	0			
		1	0	1	0	0			
		1	0	1	1	0			
		1	1	0	0	1			
		1	1	0	1	1			
1	1	1	0	1					
1	1	1	1	1					

Task		Expected response	Max mark	Additional guidance
2	c	<p>The diagram shows a logic circuit with four inputs labeled A, B, C, and D. Each input is connected to a 74HC00 NAND gate. IC5a has inputs A and B. IC5b has inputs C and C (self-loop). IC5c has inputs D and D (self-loop). IC5d has inputs C and D. The output of IC5a is connected to one input of IC6a. The output of IC5d is connected to the other input of IC6a. IC6b has inputs C and D. The output of IC6b is connected to one input of IC6c. The output of IC5b is connected to the other input of IC6c. The output of IC6c is connected to a red LED.</p>	1	Full circuit including inputs and outputs

Task		Expected response						Max mark	Additional guidance
2	d							1	<p>Actual results in truth table. (1 mark)</p> <p>Marks cannot be awarded if no evidence of simulation in 2c.</p> <p>Allow follow on error based on final circuit.</p>
		A	B	C	D	Z	NAND equivalent		
		0	0	0	0	1	1		
		0	0	0	1	0	0		
		0	0	1	0	0	0		
		0	0	1	1	0	0		
		0	1	0	0	1	1		
		0	1	0	1	0	0		
		0	1	1	0	0	0		
		0	1	1	1	0	0		
		1	0	0	0	1	1		
		1	0	0	1	0	0		
		1	0	1	0	0	0		
		1	0	1	1	0	0		
		1	1	0	0	1	0		
		1	1	0	1	1	0		
		1	1	1	0	1	0		
1	1	1	1	1	0				

Task		Expected response	Max mark	Additional guidance	
2	e	<p>The diagram shows a logic circuit with five 74HC00 NAND gates. IC5a, IC5b, and IC5c are NAND gates with two inputs each. IC5d is a NAND gate with two inputs, one of which is connected to the output of IC5b. IC6a is a NAND gate with two inputs, one of which is connected to the output of IC5a. The output of IC6a is connected to a red LED.</p>	2	<p>Removal of NAND after OR function. (1 mark)</p> <p>Removal of NAND between NOR and OR functions. (1 mark)</p> <p>Full marks to be awarded for any other correct NAND equivalent.</p>	
2	f	(i)	<p>Initial logic circuit would require 3 separate chips. NAND equivalent 2. Programmable system requiring 1.</p> <p>Fewer chips would result in: reduced construction/manufacturing time, resources and cost; final assembled product would be smaller allowing for cheaper transportation and packaging costs.</p> <p>Programmable control based system would be easier to upgrade or reprogram if operating issues were discovered in the field.</p>	3	<p>1 mark for each evaluative comment. (up to 3 marks)</p> <p>Allow follow on error based on final circuit.</p>
		(ii)	<p>Programmable control option would be the preferred solution.</p>	1	<p>Specifying the programmable control option as the preferred solution. (1 mark)</p>

Task 3 – Trapping system

Task	Expected response	Max mark	Additional guidance
<p>3</p> <p>a</p>		<p>6</p>	<p>Candidate must show that the circuit and flowchart are integrated together.</p> <p>Analogue input correctly connected. (1 mark)</p> <p>Digital inputs correctly connected. (1 mark)</p> <p>SPST relay and LED outputs connected correctly. (1 mark)</p> <p>DPDT relay and motor correctly connected. (1 mark)</p> <p>Flowchart boxes and connections correct. (1 mark)</p> <p>Flowchart contents correct. (1 mark)</p>

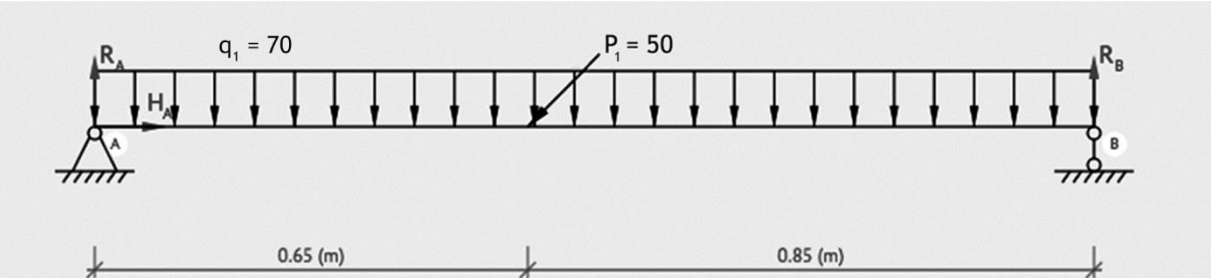
Task	Expected response	Max mark	Additional guidance
	<p style="text-align: center;">Flowchart_1</p> <pre> graph TD Start([Start]) --> I1{Input1 On?} I1 -- N --> I1 I1 -- Y --> A1{a_Input0 > 100?} A1 -- N --> I1 A1 -- Y --> S1[/Set: Output7 On/] S1 --> W1[Wait 2 s] W1 --> S2[/Set: Output7 Off/] S2 --> A2{a_Input0 > 200?} A2 -- N --> I1 A2 -- Y --> S3[/Set: Output4 On, Output5 On/] S3 --> I2{Input2 On?} I2 -- N --> I1 I2 -- Y --> Repeat[Repeat 10 times] subgraph Repeat S4[/Set: Output4 On, Output5 On/] --> W2[Wait 0.25 s] W2 --> S5[/Set: Output4 Off, Output5 Off/] S5 --> W3[Wait 0.25 s] W3 --> L{Loop?} L --> Repeat end L --> S6[/Set: Output6 On, Output7 Off/] S6 --> W4[Wait 2 s] W4 --> S7[/Set: Output7 On, Output6 Off/] S7 --> Stop([Stop]) </pre>		

Task		Expected response				Max mark	Additional guidance
3	b					6	<p>1 mark for each correct description of actual results. (3 marks)</p> <p>1 mark for each amendment in test 1. (2 marks)</p> <p>1 mark for amendment in test 3. (1 mark)</p> <p>FTE - if the candidate makes an error in the electronic circuit and makes appropriate amendments during testing this should be credited.</p>
		Planned test	Expected result	Actual result	Amendments		
		Set analogue input to 0. Press then release 'Set' switch. Adjust analogue input from 0 to over 100 (2 V), but less than 200 (4 V).	Flowchart progresses through first two decisions, motor turns on for 2 seconds, and LED 1 lights.	Chart won't progress unless switch is pressed after the analogue sensor is increased. Motor turns but no LEDs go on.	Change initial AND relationship in first two decision boxes. Add option for 1 LED in third decision box rather than looping back.		
		Repeat above test but set analogue sensor to 255 (5 V).	As above but LEDs 1 and 2 will both light.	When 'set' pressed the motor spins for 2 seconds then 2 LEDs go on.	None required.		
		When both LEDs are on, press 'Release' switch.	Both LEDs flash 10 times, then the motor spins in the opposite direction for 2 seconds.	LEDs flash 10 times but motor doesn't immediately turn on. When it does it goes the same way as before.	Change the pin numbers being switched off and on in the last two output boxes.		

Task	c	Expected response	Max mark	Additional guidance
3		<p style="text-align: center;">Flowchart_1</p> <pre> graph TD Start([Start]) --> Input2{Input2 On?} Input2 -- N --> Input2 Input2 -- Y --> A100{a_Input0 > 100?} A100 -- N --> Input2 A100 -- Y --> Set76[/Set: Output7 On, Output6 On/] Set76 --> Wait2s[Wait 2 s] Wait2s --> Set7off[/Set: Output7 Off, Output6 Off/] Set7off --> A200{a_Input0 > 200?} A200 -- N --> Set5[Set: Output5 On] Set5 --> Input2 A200 -- Y --> Set45[/Set: Output4 On, Output5 On/] Set45 --> Input2 A200 -- Y --> Input1{Input1 On?} Input1 -- N --> Input2 Input1 -- Y --> Repeat[Repeat 10 times] Repeat --> Set45_2[/Set: Output4 On, Output5 On/] Set45_2 --> Wait025_1[Wait 0.25 s] Wait025_1 --> Set4off[/Set: Output4 Off, Output5 Off/] Set4off --> Wait025_2[Wait 0.25 s] Wait025_2 --> Loop{Loop?} Loop -- N --> Repeat Loop -- Y --> Set67[/Set: Output6 Off, Output7 On/] Set67 --> Wait2s_2[Wait 2 s] Wait2s_2 --> Set6off[/Set: Output6 Off, Output7 Off/] Set6off --> Stop([Stop]) </pre>	3	1 mark for each correctly implemented correction. (3 marks)

Task		Expected response	Max mark	Additional guidance
3	d	<p>When the set switch was activated the flowchart then waited until the animal sensor was adjusted before progressing.</p> <p>When the animal sensor was increased the motor spun for 2 seconds then either one or two LEDs came on depending on whether the sensor was set above 200. While the signal to the motor was stopped, the motor kept spinning for a while longer.</p> <p>When the release switch was pressed the LEDs flashed 10 times over 5 seconds then the motor turned in the opposite direction for 2 seconds then stopped. Again, the motor span for a little longer after the signal was switched off.</p> <p>After the amendments listed in the test phase were completed the system met all the criteria in the specification.</p> <p>In the actual environment the system would need the following improvements -</p> <ul style="list-style-type: none"> ◆ A longer delay time between the release switch being pressed and the door opening to allow the user to get a safe distance away, or have a remote control trigger to activate the opening. ◆ Additional control would be required to open the door as part of the set up procedure. ◆ The closing of the door would need to be quicker to ensure the animal didn't manage to escape as it was closing. ◆ A suitable battery or portable power supply system would be required as it would not be possible to plug into a mains supply. 	6	<p>Stating whether the criteria have been met. (1 mark)</p> <p>1 mark for each evaluative comment relating to the performance of the system against the specification. (3 marks)</p> <p>1 mark for each evaluative comment relating to the suitability of the system in a real environment. (2 marks)</p>

Task 4 – Camera Track

Task	Expected response	Max mark	Additional guidance
4	<div style="text-align: center;">  </div> <div style="border: 1px solid black; padding: 10px; margin-top: 10px;"> <p>1. A beam is in equilibrium when it is stationary relative to an inertial reference frame. The following conditions are satisfied in equilibrium.</p> <p>$\Sigma F_x = 0$: $H_A - P_1 \cos(45) = 0$</p> <p>$\Sigma M_A = 0$: The sum of the moments about the pin support at the point A: $-q_1 \cdot 1.5 \cdot (1.5/2) - P_1 \sin(45) \cdot 0.65 + R_B \cdot 1.5 = 0$</p> <p>$\Sigma M_B = 0$: The sum of the moments about the roller support at the point B: $-R_A \cdot 1.5 + q_1 \cdot 1.5 \cdot (1.5 - 1.5/2) + P_1 \sin(45) \cdot 0.85 = 0$</p> <p>2. Calculate reaction of roller support at the point B: $R_B = (q_1 \cdot 1.5 \cdot (1.5/2) + P_1 \sin(45) \cdot 0.65) / 1.5 = (70 \cdot 1.5 \cdot (1.5/2) + 50 \cdot 0.7071 \cdot 0.65) / 1.5 = 67.82 \text{ (N)}$</p> <p>3. Calculate reaction of pin support at the point A: $R_A = (q_1 \cdot 1.5 \cdot (1.5 - 1.5/2) + P_1 \sin(45) \cdot 0.85) / 1.5 = (70 \cdot 1.5 \cdot (1.5 - 1.5/2) + 50 \cdot \sin(45) \cdot 0.85) / 1.5 = 72.53 \text{ (N)}$</p> <p>4. Solve this system of equations: $H_A = P_1 \cos(45) = 50 \cdot 0.7071 = 35.36 \text{ (N)}$</p> <p>5. The sum of the forces about the Oy axis is zero: $\Sigma F_y = 0$: $R_A - q_1 \cdot 1.5 - P_1 \sin(45) + R_B = 72.53 \cdot 1 - 70 \cdot 1.5 - 50 \cdot 0.7071 + 67.82 \cdot 1 = 0$</p> <p>$R_{Av} = 72.53 \text{ N}$ $R_{Ah} = 35.36 \text{ N}$ $R_B = 67.82 \text{ N}$</p> </div>	2	<p>Reaction at R_A, horizontal and vertical. (1 mark)</p> <p>Reaction at R_B. (1 mark)</p> <p>Allow follow-through error for incorrect values simulated/constructed.</p>

[END OF MARKING INSTRUCTIONS]